

	Monday - 1/9/17	Tuesday - 1/10/17	Wednesday - 1/11/17	Thursday 1/12/17	Friday - 1/13/17
8:30	Event Registration	-	-	-	-
9:00	Analog Integrated Circuit Design (Ronald Valenzuela)	Custom Designer Schematic, Symbol, SAE (Esteban Viveros)	Introduction to Analog Circuits II (Ronald Valenzuela)	Introduction to Analog Circuits III (Ronald Valenzuela)	Lab 4: Post-layout Simulations (Esteban Viveros)
9:30					
10:00					
10:30	Coffee break	Coffee break	Coffee break	Coffee break	Coffee break
11:00	Custom Designer Overview, Setup, Unix (Esteban Viveros)	Introduction to Fabrication of ICs (Victor Grimblatt)	Lab 1: Amplifier (Ideal + Common Source) (Ronald Valenzuela)	Lab 3: gm/ID (Esteban Viveros)	Evaluation
11:30					
12:00					
12:30	Lunch Break	Lunch Break	Lunch Break	Lunch Break	Lunch Break
13:00					
13:30					
14:00	Lab 0: Introduction to Custom Compiler	Introduction to Analog Circuits I (Ronald Valenzuela)	Custom Designer Layout Editor, DRC (Ronald Valenzuela)	Lab 3: gm/ID (Esteban Viveros)	Industry Talks
14:30					
15:00					
15:30	Coffee break	Coffee break	Coffee break	Coffee break	
16:00	Introduction to Semiconductor Devices (Ronald Valenzuela)	Lab 1: Amplifier (Ideal + Common Source) (Esteban Viveros)	Lab 2: NMOS Layout (Ronald Valenzuela)	Custom Designer Parasitic Extraction, Corner Analysis (Esteban Viveros)	
16:30					
17:00					
17:30	Coffee break	Coffee break	Coffee break	Coffee break	Posters - Cocktail
18:00	Keynote - Fernando Silveira Diseño analógico CMOS intuitivo y optimizado basado en gm/ID y la densidad de corriente	Keynote - Gonzalo Carvajal	Keynote - Angel Abusleme Diseño de Chips en Chile: Inicios, avances y desafíos	Keynote - Douglas Gouddat	
18:30					
19:00					
19:30					