

	Monday 1/8/18	Tuesday 1/9/18	Wednesday 1/10/18	Thursday 1/11/18	Friday 1/12/18
8:30	Event registration				
9:00	Introduction to Digital Design	Fundamental of CMOS II	Design Compiler I	IC Compiler I	Lab 7: Final review
9:30	Victor Grimblatt	Ronald Valenzuela	Esteban Viveros	Gonzalo Fernandez	(Lab - B)
10:00	(Lab - B)	(Lab - B)	(Lab - B)	(Lab - B)	
10:30	Coffee break	Coffee break	Coffee break	Coffee break	Coffee break
11:00	Fundamental of CMOS I	Simulation of Digital Circuits	Design Compiler II	IC Compiler II	Evaluation
11:30	Ronald Valenzuela	Victor Grimblatt	Esteban Viveros	Gonzalo Fernandez	(Lab - B)
12:00	(Lab - B)	(Lab - B)	(Lab - B)	(Lab - B)	
12:30	Lunch	Lunch	Lunch	Lunch	Lunch
13:00					
13:30	Verilog for Synthesis	Introduction to fabrication of ICs	Lab 3: Synthesis with Design	Lab 5: Place & Route with ICC	Industry talks (EIE 4-5 /EIE 4-6)
14:00	Victor Grimblatt	Ronald Valenzuela	Compiler	(Lab - B)	
14:30	(Lab - B)	(Lab - B)	(Lab - B)		
15:00	Coffee break	Coffee break	Coffee break	Coffee break	
15:30	Lab 1 Verilog	Lab 2 verilog	Lab 4: Synthesis with Design	Lab 5: Place & Route with ICC	
16:00	(Lab - B)	(Lab - B)	Compiler	(Lab - B)	
16:30			(Lab - B)		
17:00					Event closure
17:30	Break	Break	Break	Break	Posters - Cocktail 4th Floor
18:00	Keynote - Carlos Silva	Keynote - Francois Rivet	Keynote - Mauricio Rodriguez	Keynote - Ricardo reis	
18:30	ENERGY HARVESTING	ELECTRONICS AND MATHEMATICS:	MODELADO DEL CANAL INALÁMBRICO	LOW POWER CHALLENGES IN IOT	
19:00	(EIE 4-5 /EIE 4-6)	AN INTRODUCTION TO RFIC DESIGN	PARA FUTURAS COMUNICACIONES	AND IOE	
19:30		(EIE 4-5 /EIE 4-6)	MÓVILES SG (EIE 4-5 /EIE 4-6)	(EIE 4-5 /EIE 4-6)	